**BIRLA INSTITUTE OF SCIENCE AND TECHNOLOGY PILANI, HYDERABAD CAMPUS**

**DIGITAL DESIGN LABORATORY (Session 2021-22)**

**Workbook**

**Experiment -8**

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**Complete ID of the student: 2020A7PS2081H**

**Title of Experiment: Experiment 8**

**Problem 1:**

**Implement a ‘Counter’ using D Flip-Flop circuit for a given State diagram (As mentioned in the Manual)**

**(Provide proper snapshots and Show the graphical output)**

**Diagram, schematic

Description automatically generatedGraphical user interface

Description automatically generated**

**Problem 2:**

**Implement a “Gray Counter” using JK Flip-Flop circuit for a given state diagram (as mentioned in the manual)**

**(Provide proper snapshots and show the graphical output)**

**Chart, box and whisker chart

Description automatically generatedA screenshot of a computer

Description automatically generated with medium confidence**